



# THE MOMBASA POLYTECHNIC UNIVERSITY COLLEGE

(A Constituent College of Jkuat)

# Faculty of Engineering and Technology

## DEPARTMENT OF COMPUTER SCIENCE & INFORMATION TECHNOLOGY

DIPLOMA IN INFORMATION TECHNOLOGY (DIT 10M) YR 2 SEM I

### ECS 2208: MATHEMATICS III

### END OF SEMESTER EXAMINATIONS

SERIES: AUGUST/SEPTEMBER 2011

TIME: 2 HOURS

**Instructions to Candidates:** 

Answers **MUST** be written clearly within the answer booklets provided with the exam paper Answer questions **ONE (COMPULSORY)** and any other **TWO** questions from section **B** This paper consists of **THREE** printed pages

#### **SECTION A (30 MARKS) – Answer all Questions in this section**

#### **Question 1 (Compulsory)**

a)	Solve $2 \qquad 1 \qquad 1$	
	$\frac{2}{x+1} - \frac{1}{x-2} = -1$ (i)	(3 marks)
	$2(x-2)^2 - 4 = y$ $4x - y = 2$	
b)	(ii) Using 4 bit representation, evaluate the following arithmetic's in two's complement i	(4 marks) notation
	(i) $14-8$ (ii) $7-13$	(3 marks) (4 marks)
c)	Draw the symbol of a <b>THREE</b> input NOR operator and list all the possible outputs	(4 marks)
d)	Represent 13 as a BCD with the following methods Excess 3 and 2421	(4 marks)
e)	Describe how Repetition Codes can be used as an error detection scheme	(3 marks)
f)	Outline <b>FOUR</b> sources of errors in a transmitted signal	(3 marks)
g)	Name <b>TWO</b> alphanumeric coding systems in use today	(2 marks)
<u>SE</u>	ECTION B (40 MARKS) – Answer any TWO questions from this section	
	ECTION B (40 MARKS) – Answer any TWO questions from this section uestion 2 (20 marks)	
Qu		(4 marks)
Qu a)	The system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z)	(4 marks) (4 marks)
Qu a) b)	<b>Lestion 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$	
Qu a) b) c)	<b>Lestion 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression	(4 marks)
Qu a) b) c)	<b>Lestion 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression a simple circuit with equivalent output and provide its truth table <b>Lestion 3 (20 marks)</b>	(4 marks)
Qu a) b) c) Qu	<b>The section 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression a simple circuit with equivalent output and provide its truth table <b>the section 3 (20 marks)</b> Nepresent the binary equivalent of decimal number 237 in gray code	(4 marks) and represent (12 marks)
Qu a) b) c) Qu a)	<b>Lestion 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD + A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression a simple circuit with equivalent output and provide its truth table <b>Lestion 3 (20 marks)</b> Represent the binary equivalent of decimal number 237 in gray code Differentiate between weighted and Non-weighted codes Using the method of 4-bit two's complement evaluate the following (i) $7-13$	(4 marks) and represent (12 marks) (4 marks) (4 marks) (3 marks)
Qu a) b) c) Qu a) b)	<b>Lestion 2 (20 marks)</b> Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression a simple circuit with equivalent output and provide its truth table <b>Lestion 3 (20 marks)</b> Represent the binary equivalent of decimal number 237 in gray code Differentiate between weighted and Non-weighted codes Using the method of 4-bit two's complement evaluate the following	(4 marks) and represent (12 marks) (4 marks) (4 marks)
Qu a) b) c) Qu a) b)	Form a system of NAND gates that can perform the operation of NOR gate Draw a truth table for P'T' (P+Z) $(A+C) \cdot (AD+A \cdot \overline{D}) + A \cdot C + C$ Draw the logic circuit for the Boolean expression a simple circuit with equivalent output and provide its truth table <b>uestion 3 (20 marks)</b> Represent the binary equivalent of decimal number 237 in gray code Differentiate between weighted and Non-weighted codes Using the method of 4-bit two's complement evaluate the following (i) $7-13$ (ii) $11-5$	(4 marks) and represent (12 marks) (4 marks) (4 marks) (3 marks)

**Question 4 (20 marks)** 

a) Find the solution set for the following system of equations. Use crammer's rule

was travelling at 20km/hr faster than Njeri, determine the Peter's speed.

4x - 2y - 3z = 8	
5x + 3y - 4z = 4	
6x - 4y - 5z = 12	
	(10 marks)

(x + y)<sup>7</sup>
 b) Expand the expression . Using the expansion, approximate the value of 1.97<sup>7</sup> to 3 d.p. (5 marks)
 c) Peter and Njeri travelled from Mombasa to Nairobi through a distance of 400km. Njeri left Mombasa half an hour earlier than Peter. Njeri arrived two hours later after Peter did. If Peter

#### **Question 5 (20 marks)**

a) List any <b>FOUR</b> sources of errors in a transmitted signal	(4 marks)
b) Explain the effects of errors in a transmitted signal affect communication	(6 marks)
c) Differentiate the Parity check and Repetition code as error detection methods	(6 marks)
<ul> <li>d) Rewrite the signals provided below with both even and odd parity check</li> <li>(i) 1010111</li> <li>(ii) 1001101</li> </ul>	(2 marks) (2 marks)

(5 marks)