



THE MOMBASA POLYTECHNIC UNIVERSITY COLLEGE

(A Constituent College of JKUAT)

Faculty of Engineering & Technology

DEPARTMENT COMPUTER SCIENCE & INFORMATION TECHNOLOGY

DIPLOMA IN INFORMATION COMMUNICATION TECHNOLOGY DICT 2K 11M/DICT 11M

AMA 2110: COMPUTATIONAL MATHEMATICS

END OF SEMESTER EXAMINATIONS

SERIES: DECEMBER 2011

TIME: 2 HOURS

Instructions to Candidates: You should have the following for this examination - Answer Booklet This paper consist of FIVE questions in TWO sections A & B Answer question ONE (COMPULSORY) and any other TWO questions Maximum marks for each part of a question are as shown This paper consists of FOUR printed pages

SECTION A (COMPULSORY)

QUESTION ONE (20 MARKS)

CD D

a) Define the term 'set'		[1mark]
b) Define a Venn diagram		[1mark]
c) Find the values of X and Y in the follo	owing linear system using Cramer's rule	[2marks]
57-41-2		
6X-5Y=1		
 d) What is the complement a of a null set e) State the laws of Boolean algebra f) Given A={1,2,3,4}, B={3,4,5} and C 	t and state why ={5,6,7} prove the distribution law	[2marks] [3marks]
g) Given the logic circuit below, give its	Boolean expression	[3marks] [3marks]
fcvv B		
h) Draw the truth tables for AND, OR ar	nd NOT logical operations	[3marks]
i) Perform the following binary addition	1	
1010 +0111		[2marks]
SECTION B (ANSWER ANY TWO Q	<u>UESTIONS)</u>	
QUESTION 2 (20 MARKS)		
a) Define Boolean algebra		[1mark]
b) Construct a truth table for the Boolean functions: F=XYZ, F=XY+Z and F=X	n functions with three inputs XYZ and d K+YZ	erive the following [9marks]
c) Draw a simple analogy of the AND ga	ate and construct its truth table	[6marks]
d) Express the decimal number 567:		
i in binary		[1 marks]
ii in octal		[1marks]
e) Draw the circuit symbols of NAND g	gate and NOR gate	[2marks]

QUESTION THREE (20 MARKS)



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(-5	-1	1)
10	2	3
$\left(1\right)$	-2	6)

c)	(i) 567 octal to binary(ii) 684 decimal to binary	[2 marks] [2marks]
d)	Draw the circuit symbol for OR gate and construct its truth table with three inputs.	[6marks]
e)	Draw the logic circuit for the following expression	[3marks]
	Z=A.B+C.D	
f)	Define a matrix	[1mark]
g)	Express the number 747 ⁸ in: Hexadecimal	[2 marks]