



**TECHNICAL UNIVERSITY OF MOMBASA**  
***Faculty of ENGINEERING & TECHNOLOGY***

DEPARTMENT OF ELECTRICAL & ELECTRONIC ENGINEERING

FACULTY OF ENGINEERING and Technology in Conjunction with Kenya Institute of  
Highways & Building Technology (KIHBT)

HIGHER DIPLOMA IN ELECTRICAL AND ELECTONIC ENGINEERING

**EEE 3108: COMPUTER AIDED DESIGN PROJECT**

END OF SEMESTER EXAMINATIONS

**SERIES:** AUGUST 2013

**TIME:** 2 HOURS

**INSTRUCTIONS:**

1. You are required to have the following for this examination;
  - Answer Booklet
2. Section A is compulsory, answer One question in Each of the remaining Sections
3. This paper consists of Two printed pages.

**SECTION A- THIS SECTION IS COMPUSORY.**

QUESTION 1 (Compulsory)

- a) State and explain any TWO design layouts for an electrical installation. (6marks)
- b) Explain the factors to be considered in the design process of electrical work. (14marks)

**SECTION B ANSWER ONE question in this section.**

QUESTION 2

For the circuit of figure Q2 reproduce the circuit in 2D using AutoCAD. Avoid labels and key. (20marks)

QUESTION 3

For the amplifier circuit of figure Q3, reproduce the circuit in 2D using Autocad. Avoid labels. (20marks)

**SECTION C Answer ONE question in this section.**

QUESTION 4

Draw the network system of figure Q4 in 3D using Autocad. Avoid labels. (20marks)

QUESTION 5

Draw the cable of figure Q5 in 3D using Autocad. Avoid labels. (20marks)