# UNIVERSITY EXAMINATION 2016/2017 <br> THIRD YEAR SECOND SEMESTER EXAMINATION FOR THE DEGREE OF BACHELOR OF TECHNOLOGY AND APPLIED PHYSICS EEE 4350: DIGITAL ELECTRONICS \& DEVICES END OF SEMESTER EXAMINATION <br> SERIES: DECEMBER 2016 <br> TIME: 2 HOURS 

DATE: Pick DateSelect MonthPick Year

## Instructions to Candidates

You should have the following for this examination
-Answer Booklet, examination pass and student ID
This paper consists of FIVE questions. Attempt Question ONE (Compulsory) and any other TWO Questions Do not write on the question paper.

## Question ONE

a. De-Morganize the Boolean expression below and implement the simplified expression using a minimum number of NAND gates.

$$
\begin{equation*}
\mathrm{Y}=(\overline{\mathrm{A} \cdot \mathrm{~B}}+\mathrm{C}) \cdot(\mathrm{A}+\overline{\mathrm{B} \cdot \mathrm{C}}) \tag{8marks}
\end{equation*}
$$

b. With the aid of a block diagram, XOR implementation and truth table describe the operation of a fulladder.
(9 marks)
c. In a 4-stage ripple counter, the propagation delay of a Flip-flop is 50 ns . If the pulse width of the strobe is 30 ns , find the maximum frequency at which the counter operates reliably.
d. For the circuit given in Figure Q1, sketch the output signal at $Q_{2}$ for five clock periods. Assume that the two flip-flops are initially cleared.


## Figure Q1

e. Tyrone Shoelaces has invested a huge amount of money into the stock market and doesn't trust just anyone to give him buying and selling information. Before he will buy a certain stock, he must get input from three sources. His first source is Pain Webster, a famous stock broker. His second source is Meg A. Cash, a self-made millionaire in the stock market, and his third source is Madame LaZora, a world-famous psychic. After several months of receiving advice from all three, he has come to the following conclusions:
i. Buy if Pain and Meg both say yes and the psychic says no.
ii. Buy if the psychic says yes.
iii. Don't buy otherwise.

Construct a truth table and find the minimized Boolean function to implement the logic telling Tyrone when to buy. Implement the minimized Boolean function.

## Question TWO

a. Prove that $B \oplus(B \oplus A . C)=A C$
b. Use a Karnaugh Map to simplify the following Boolean expression:

$$
F(w, x, y, z)=\sum m(1,5,6,9,13)
$$

that has the don't care conditions

$$
\begin{equation*}
d(w, x, y, z)=\sum m(4,7,12) \tag{5marks}
\end{equation*}
$$

c. Show the states of a 4-bit SISO register for data input 1101 using a block diagram, waveforms and transition table. Assume the registers contain ones initially. Use positive-edge triggered D-flip-flops
(10 marks)

## Question THREE

a. Define the term race around as used in flip-flops.
b. In the flip-flop circuit of Figure Q3.1 show that if:
i. $\quad P_{r}=0$ and $C_{r}=1$, then $Q=1$ (independent of $S, R$, and $C K$ )
ii. $\quad P_{r}=1$ and $C_{r}=0$, then $Q=0$ (independent of $S, R$, and $C K$ )
iii. $\quad P_{r}=C_{r}=1$, then it functions as a clocked SR flip-flop
(10 marks)


Figure Q3.1
c. Figure Q3.2 shows a positive edge-triggered D-type flip-flop. Verify its operation.


Figure Q3.2

## Question FOUR

a. Design a J-K counter that goes through states $2,4,5,7,2,4 \ldots \ldots$
b. Differentiate between combinational and sequential logic circuits.

## Question FIVE

a. Perform the following operations:
i. $\quad$ D3F $\mathrm{F}_{16}-75 \mathrm{C}_{16}$ (using 2's complement)
ii. $396_{10}+863_{10}$ (using Excess- 3 code)
iii. $10101100111_{2}$ to Gray
b. i. Find the Boolean expression and Truth Table that describes the circuit of Figure Q5.
ii. Design the circuit in (i) using minimum number of NOR gates


Figure Q5

